

ABSTRACT OF THE DISCLOSURE

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Disclosed is an LCD panel, an LCD including the LCD panel, and a driving
(method for high-speed responses. The present invention comprises a plurality of gate
lines; a plurality of data lines; a common electrode line formed in the horizontal direction
5 with vertical branches, and formed at a predetermined area between a gate line and its
adjacent gate line; a first pixel electrode formed at an odd row of an odd column and
even row of an even column among an area formed by a the data line and a gate line;
and a second pixel electrode formed at an odd row of an even column and an even row
of an odd column of the area, and having a polarity different from the first pixel
10 electrode. The first pixel electrode is formed at an area respectively divided by an odd
data line and its subsequent adjacent even data line and by connecting an odd gate line
and its subsequent adjacent common electrode line, and formed at an area respectively
divided by an even data line and its subsequent adjacent odd data line and by
connecting an even common electrode line and its subsequent adjacent gate line; and
15 the second pixel electrode is formed at an area respectively surrounded by an even
data line and its subsequent adjacent odd data line and connected to an odd common
electrode line and its subsequent adjacent gate line, formed at an area respectively
divided by an odd data line and its subsequent adjacent even data line and connected
to an even gate line and its subsequent adjacent common electrode line.